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G3-ANCL02-V2: Genie Nano-CL Application Note

Configuring Genie Nano-CL and Teledyne DALSA Frame Grabbers

For Nano-CL models with P/N: G3-Cxxx-xxxxx

Overview

Genie Nano-CL (CameraLink) cameras require a connection to a frame grabber to acquire images. This application note describes how to configure a Genie Nano-CL with a Teledyne DALSA frame grabber.

The Genie Nano-CL is GenCP compliant; its features are defined using an XML file and can be accessed using software that supports the GenCP protocol.

For information on installing and connecting the Genie Nano-CL, refer to the application note G3-ANCL02-V1: Genie Nano-CL Application Note.

Tap Configurations

Tap configurations determine how data is output from the camera and received by the frame grabber; both the frame grabber and the camera must be set to the same tap configuration to enable correct image acquisition. Each tap represents a data communication lane that can transmit one pixel (for example, 8 or 10 bit) per pixel clock tick.

The Genie Nano CL tap configuration is determined by the device firmware; each firmware supports a unique tap configuration. To change the tap configuration, the corresponding firmware must be uploaded to the camera. Currently, 3 firmware versions are available that provide the following tap configurations:

- Base, 8-bit (2 or 3 tap, model dependent)
- 8 tap, 8-bit
- 10 tap, 8-bit (factory default firmware)

For CameraLink devices, the pixel clock is 85MHz, therefore the maximum throughput is:

- 850 MB/s (6.8 Gbit/s) for 10 tap (Deca), 8-bit configurations
- 680 MB/s (5.44 Gbit/s) for 8 tap (Full), 8-bit configurations
- 510 MB/s (4.08 Gbit/s) for 6 tap (Medium), 8-bit configurations
- 255 MB/s (2.04 Gbit/s) for 3 tap (Base), 8-bit configurations



1. Start the Sapera CamExpert Application

The Sapera CamExpert application is included as part of the Sapera LT SDK. It is Teledyne DALSA's camera and frame grabber interfacing tool that allows you to quickly validate hardware setup, change parameter settings, and test image acquisition. It is available from the Windows Start Menu or desktop shortcut.



2. Select the Frame Grabber

In the Device Selector panel, use the Device drop-down menu to select the Teledyne DALSA frame grabber the Genie Nano-CL is connected to.



The available servers for the frame grabber are determined by its currently loaded firmware (default firmware is 8 tap 8-bit (Camera Link Full)



Note, Genie Nano CL 10-tap models require the frame grabber firmware configuration to be *80-Bits Camera Link*.

If necessary, use the Teledyne DALSA Device Manager tool to change the firmware configuration; refer to Performing a Frame Grabber Firmware Update.

The Video Status bar indicates if the pixel clock, frame valid, and line valid signals are present by being displayed in green:

Video status: Pixel Clock 1 Pixel Clock 2 Pixel Clock 3 Frame Valid Line Valid PoCL PoCL 2

The number of available pixel clock signals depend on the tap configuration (Base, Medium, Full or Deca); the number of PoCL signals is camera dependent.

If the camera detection parameter are properly configured, both the frame grabber and camera parameters are displayed.

Parameters		×
Category	Parameter	Value
Board	Camera Type	Areascan
Basic Timing	Color Type	Monochrome
Advanced Control	Horizontal Active (in Pixels)	1280
External Trigger	Horizontal Offset (in Pixels)	0
	Vertical Active (in Lines)	1024
Image Buffer and ROI	Vertical Offset (in Lines)	0
Attached Camera - CameraLink_1	Pixel Clock Input Frequency (MHz)	85
Camera Information	Camera Sensor Geometry Setting	Custom
Sensor Control	Bit Transfer Rate Status	0
I/O Controls	PoCL	Disabled
Counter And Timer Control	PoCL Status	Not Active
Advanced Processing		
Image Format Controls		
Camera Link Transport Layer		
File Access Control		
Parameters		

3. Verify the Camera Settings

In the Camera Information category, the *Feature Manufacturer Info* feature in the Camera Information displays the configuration (for example, 80-bit 10 Taps Design). The *Feature Model Name* feature displays the camera model which identifies if the camera is monochrome or color (for example, M5100 (monochrome) or C5100 (color)).

Adjust any camera settings as required and save the settings to the camera; it is recommended that the camera Power-Up Configuration be set to use this user set.

Verifythe Width and Height feature settings; the default values for the Genie Nano-CL M25 and M16 camera models are 5120 (5K) and 4096 (4K), respectively.

4. Save the Genie Nano-CL Camera Settings

Current Genie Nano-CL camera settings can be saved a User Set that can be loaded when the camera is reset or power on. Use the Power-Up Configuration dialog, available through the Camera Information category, to specify feature settings.

Parameters			×	
Category	Parameter	Value		
Board	Manufacturer Name	Teledyne DALS	A	
Basic Timing	Device Family	Genie		
Advanced Control	Model Name	G3-CC10-C510		
	Device Version	1.00 Beta	Power-up Configuration	
External Irigger	Manufacturer Part Number		Camera Power-up configuration	_
Image Buffer and ROI	Manufacturer Info	80-bit 10 Taps		
Attached Camera - Cam	Firmware Version	1CA21.0005	UserSet1	
Camera Information	Serial Number	S1139051	1	
Sensor Control	Device User ID		Load / Save Configuration	
I/O Controls	Device Built-In Self Test	Press	UserSet 1	
Counter And Timer Control	Device Built-In Self Test Status	Passed	1 ,	
	Device Built-In Self Test Status All	0	Save Load	
Advanced Processing	Device Reset	Press		
Image Format Controls	Device Temperature Selector	Internal	1	
Camera Link Transport Layer	Device Temperature	58.995041	Close	
File Access Control	Power-up Configuration	Setting	Close	
	<< Less			

5. Verify the Frame Grabber Settings

Adjust any frame grabber settings as required. Verify that the Color Type, Horizontal Active, Horizontal Offset, Vertical Active and Vertical Offset parameters correspond to those output by the camera.

2	tegory		Parameter	Value
-	Board		Camera Type	Areascan
	Basic Timing	1	Color Type	Monochrome
	Advanced Control	1	Horizontal Active (in Pixels)	1280
	External Trigger		Horizontal Offset (in Pixels)	0
			Vertical Active (in Lines)	1024
	Image Buffer and ROI		Vertical Offset (in Lines)	0
Ξ	Attached Camera - CameraLink_1		Pixel Clock Input Frequency (MHz)	85
	Camera Information		Camera Sensor Geometry Setting	Custom
	Sensor Control		Bit Transfer Rate Status	0
	I/O Controls		PoCL	Disabled
	Counter And Timer Control		PoCL Status	Not Active
	Advanced Processing			
	Image Format Controls			
	Camera Link Transport Layer			
	File Access Control			

For Bayer cameras, set the Color Type to "Bayer Mosaic".

Pa	rameters				×
Ca	tegory		Parameter	Value	
⊡	Board		Camera Type	Areascan	
	Basic Timing		Color Type	Monochrome	-
	Advanced Control		Horizontal Active (in Pixels)	Monochrome	
			Horizontal Offset (in Pixels)	Bayer mosaic	
	External Trigger Image Buffer and ROI		Vertical Active (in Lines)	1024	
			Vertical Offset (in Lines)	0	
	Attached Camera - CameraLink_1		Pixel Clock Input Frequency (MHz)	85	
	Camera Information		Camera Sensor Geometry Setting	Custom	
	Sensor Control		PoCL	Disabled	
	I/O Controls		PoCL Status	Not Active	
	Counter And Timer Control				
	Advanced Processing				
	Image Format Controls				
	Camera Link Transport Layer				
	File Access Control				

6. Save the Frame Grabber Settings

Use the CamExpert **File > Save** menu command to save these settings as a *.ccf* (camera configuration file).

🚳 CamExpert - [Untitled]						
File	View Pre-Proce	ssing	Tools			
	New	Ctrl	+N			
	Open	Ctrl	+0			
	Save	Ctr	I+S			
	Save As					
	Open Image					
	Save Image					
	Preferences		>			
	Recent File					
	Exit					

The save dialog allows you to provide descriptive information relevant to the particular .ccf file.

Save the frame grabb	er configuration file	×
Camera Configuration	Description	
Company Name:	No Name	
Model Name:	No Name	
Camera Mode:	Default Area Scan 10 taps Parallel Mono	
Configuration:	Default Area Scan 10 taps Parallel Mono	
File Information		
File name:	N_No_Name_Default_Default	
Save as	Camera configuration file (.ccf)	
Current	C:\Program Files\Teledyne DALSA\Sapera\CamFiles\L	
	Select Custom Directory Browse,	
	Cancel	

This file is then available in the CamExpert Configuration drop-down list to load the camera file parameter settings to the frame grabber.

Device Selector			×
Device:	😅 Xtium-CL_PX4_1 🍃 CameraLink 10-Tap/8-Bit Mono	•]
Configuration:	Select a camera file (Optional)	-	
Detection:	B-웹 Sentech B-웹 TELI B-웹 Teledyne DALSA B-웹 Thales	^	
Parameters	ia⊴a Toshiba ia⊴a UNIQ Vision		×
Category	🗄 🚛 User's Configuration File		
Basic Timing	🗄 📾 Teledyne DALSA		
Advanced Cor	'⊟ PX4 ⊡⊒⊒ Area Scan 10 taps Parallel Mono		
External Irigge		\checkmark	

In the Sapera LT API, this configuration file can be referenced by the SapAcquisition constructor when creating a SapAcquisition object for the frame grabber.



Note, if the frame grabber is reset, power-cycled, or the connected application restarted, the frame grabber intializes with default settings; to restore saved settings a configuration file must be reloaded.

For more information, refer to the Sapera LT 8.30 Getting Started Manual for Frame Grabbers.

Performing a Frame Grabber Firmware Update

Start the Teledyne DALSA Device Manager application, available through the start menu:



On the Firmware Update tab, select the required configuration and click **Start Update**. Refer to the frame grabber documentation for information on supported user programmable firmware configurations.

🔭 Teledyne DALSA I	☆ Teledyne DALSA Device Manager v:3.76						
<u>F</u> ile <u>T</u> ools <u>H</u> elp							
Firmware Update Manag	jer						
Start Update Save	Start Update Save Config file Load Config File 🔽 Same Configuration For All Devices						
Device	Field	Value					
Xtium-CL_MX4_1	Serial Number	H0498017					
Update Firmware 🗹	Device Version	0x000000000000000					
	ACU/DTE + PCIe Interface	1.20.01.0230					
	Configuration	1 x Full Camera Link			-		
	Information	80-Bits Camera Link					
	Firmware State	1 x Full Camera Link					

The Output window displays if the update was successful.

Output	
[12:09:48] (Xitum-CL_PX4_1) ·· Update of ACU/DTE + PCIe Interface in progress [12:09:57] (Xitum-CL_PX4_1) ·· Successfully updated ACU/DTE + PCIe Interface. [12:09:57] (Xitum-CL_PX4_1) ·· Beset in progress [12:09:58] (Xitum-CL_PX4_1) ·· Device reset complete. [12:09:58] (Xitum-CL_PX4_1) ·· Device reset state [12:09:58] (Xitum-CL_PX4_1) ·· Device's firmware State	^
	v

POCL

If using Power-over-CameraLink to power the Genie Nano-CL, enable PoCL on the frame grabber (by default, it is disabled) using the PoCL parameter, available in the frame grabber's Basic Timing category.



The PoCL Status parameter can be used to verify if PoCL is active:

PoCL	Enable
PoCL Status	Active

The CamExpert video status bar displays the PoCL signals in green:

Video status: Pixel Clock 1 Pixel Clock 2 Pixel Clock 3 Frame Valid Line Valid PoCL PoCL 2

When PoCL is enabled and the PoCL Status is "Not active" (that is, false), this indicates that the camera is not PoCL compliant, the wrong cable is used, or the camera is not connected.



External power must be provided to the frame grabber to use PoCL; if not the following status message is displayed when attempting to enable PoCL on the frame grabber:

[14:41:30] (Xtium-CL_PX4_1) Error: "CorAcqSetPrms" <Acq module> - External power not present ()

Refer to the frame grabber documentation for information on how to connect power to the frame grabber to use PoCL.

Frame Grabber Image Width Considerations

Depending on the camera/frame grabber pair, each may have different image width increments. With certain camera image width settings, the frame grabber may not be able to exactly match the same width due to a different increment step. In this case, the frame grabber width can be set to the next smaller/larger increment step to either crop the camera image output, or pad the image outpu from the frame grabber with extra pixels.

That is, when setting the frame grabber Horizontal Active, the line may contain cropped or padded pixels if the multiple of increment does not coincide with the camera horizontal width.

The following table provides the possible values for image and width height features for the Genie Nano-CL 16M and 25M (Monochrome and Bayer) cameras:

Feature	Minimum Value	Maximum Value	Increment in Pixels
Height	256	4096 (Genie Nano-CL 16M) 5120 (Genie Nano-CL 25M)	16
Width	16	4096 (Genie Nano-CL 16M) 5120 (Genie Nano-CL 25M)	16

The following table provides the possible values for Horizontal Active and Vertical Active parameters for a typical Teledyne DALSA frame grabber, such as the Xtium-CL PX4:

Parameter	Minimum Value	Maximum Value	Increment in Pixels
Horizontal Active	40	8-bits/pixel x 64k Pixels 16-bits/pixel x 32k Pixels 32-bits/pixel x 16k Pixels 64-bits/pixel x 8k Pixels	10 (10-tap configuration) 8 (8-tap configuration)
Vertical Active	1	16777215	1

The frame grabber tap configuration setting can be verified using the Camera Sensor Geometry Setting parameter.

-		P	11/1	
a	egory	Parameter	Value	
Ξ	Board	Camera Type	Areascan	
	Basic Timing	Color Type	Monochrome	
	Advanced Control	Pixel Depth	8	
	External Trigger	Horizontal Active (in Pixels)	2000	
	Image Puffer and POI	Horizontal Offset (in Pixels)	0	
114	image burrer and KOI	Vertical Active (in Lines)	481	
Ξ	Attached Camera - Cam	Vertical Offset (in Lines)	0	
	Camera Information	Pixel Clock Input Frequency (MHz)	20	
	Sensor Control	Data Valid	Disabled	
	I/O Controls	Camera Sensor Geometry Setting	1X10-1Y	
	Counter And Timer Control	PoCL	Disabled	
		PoCL Status	Not Active	
	Camera Link Transport Layer File Access Control			
	Camera Link Transport Layer File Access Control			
CDto	Camera Link Transport Layer File Access Control	ng Ind how the multitap data is output by the car video line. Refer to the carnera specifications	nera. Combined for information on its	

For example, when in 10-tap configuration, the frame grabber receives 10 pixels per clock, therefore the increment for the Horizontal Active is also 10. If the camera width is set to 4992, the frame grabber Horizontal Active is set to 5000 to capture the entire line, resulting in 8 padded pixels.

Horizontal Line

4992 Image Pixels

Padding

-Horizontal Active = 5000-



Note, padded pixels contain any data that was previously written in the image buffer memory location.